

42390P13736

PATENT

REMARKS

Claims 1-9, 12-18, 22-24, 27-29, and 32-39 are pending in the application. Claims 1, 24, and 34 are independent claims.

The abstract of the disclosure has been objected to as failing to sufficiently disclose the present invention. The abstract has been amended, and it is respectfully submitted that the amended abstract sufficiently discloses the present invention.

Claims 1, 6-9, 12-14, 16-18, 22-24, 32, and 33 have been rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,401,208 ("Davis"). Claims 1-5, 24, and 34-36 have been rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,926,631 ("McGarvey"). Claims 1, 24, and 34 have been rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,689,638 ("Sadovsky"). Claims 1, 24, and 34 have been rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,393,420 ("Peters"). Claims 1, 24, and 34 have been rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,389,511 ("Kedem"). Claim 37 has been rejected under 35 U.S.C. §103(a) as being unpatentable over McGarvey in view of Applied Cryptography by Bruce Schneier, John Wiley & Sons, 1996 ("Schneier"). Claim 15 has been rejected under 35 U.S.C. §103(a) as being unpatentable over McGarvey in view of Schneier and U.S. Patent Publication No. 2003/0037237 ("Abgrall"). Claims 38 and 39 have been rejected under 35 U.S.C. §103(a) as being unpatentable over McGarvey.

Claim 1 has been amended to include a limitation that the one or more execution units are to lock the memory. This limitation is not disclosed by Davis, McGarvey, Sadovsky, Peters, Kedem, Schneier, or Abgrall.

The examiner argues, with respect to the rejection of claims 4 and 5, that McGarvey, in column 9, lines 16-21, discloses execution units that lock a cache memory to prevent replacement of lines of an authenticated code module that is stored in the

□

42390P13736

PATENT

cache memory. This argument is respectfully traversed. McGarvey describes loading a Java Runtime Environment *from* a Java code cache (emphasis added), and obtaining an extended security layer from a server (see column 9, lines 23-25). The extended security layer is used to provide an authenticated key ring for verification of code signatures and for use in establishing Secure Socket Layer or other secure network connections (see column 5, lines 23-28). McGarvey does not describe locking a memory.

Because none of the references, alone or in combination, disclose all of the limitations of amended claim 1, amended claim 1 cannot be anticipated or rendered unpatentable by the references. Claims 2, 4-9, 12-18, 22, and 23 depend on amended claim 1, and therefore include at least one limitation that is not disclosed by the references.

Based on the above, it is respectfully requested that the rejections of claims 1-2, 4-9, 12-18, 22, and 23 be withdrawn. Claims 3, 24, 27-29, and 32-29 have been canceled.

42390P13736

PATENT

CONCLUSION

Based on the foregoing, it is respectfully submitted that all of the rejections of claims 1-2, 4-9, 12-18, 22, and 23 have been overcome, and that claims 1-2, 4-9, 12-18, 22, and 23 are in condition for allowance. The applicant therefore respectfully requests the issuance of a Notice of Allowance. Please charge any necessary fees, including extension fees, to our Deposit Account No. 50-0221.

Respectfully submitted,

Date: November 14, 2006



Thomas R. Lane
Registration No. 42,781